

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,873,318 B1
DATED : March 29, 2005
INVENTOR(S) : Wong

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page,

Item [57], ABSTRACT,

Line 9, delete “flips-flops” and insert -- flip-flops --.

Column 1,

Line 42, delete “interfacing” and insert -- interfering --.

Line 42, delete “ether” and insert -- either --.

Column 2,

Line 2, delete “Afterwords” and insert -- Afterwards --.

Line 5, delete “embodiments” and insert -- embodiment --.

Column 3,

Line 25, delete “<100” and insert -- >100 --.

Line 33, delete “clocks” and insert -- clock --.

Column 4,

Line 8, delete “syn” and insert -- sync --.

Line 61, delete “selected” and insert -- select --.

Column 5,

Line 27, delete “pixels” and insert -- pixel --.

Line 32, delete “resynchronization” and insert -- re-synchronization --.

Line 34, delete “fine” and insert -- finer --.

Line 47, delete “211” and insert -- 221 --.

Column 6,

Line 3, after “from” delete “the”.

Line 25, after “signal” delete “data first stage” and insert -- at a final stage --.

Line 32, after “video” delete “signal”.

Line 45, delete “integral” and insert -- integrated --,

Line 55, insert -- the -- before “re-clocking”.

Line 55, delete “on figured” and insert -- configured --.

Column 7,

Line 22, after “interfering” delete “the”.

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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 8,
Line 17, delete "blocked" and insert -- clocked --.

Signed and Sealed this

Twelfth Day of July, 2005



JON W. DUDAS
Director of the United States Patent and Trademark Office